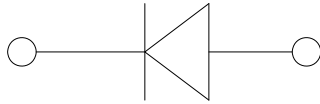


Low profile package
Ideal for automated placement
Glass passivated chip junction
High forward surge capability
Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

For use in general purpose rectification of power supplies, inverters, converters, and freewheeling diodes for consumer and telecommunication.



: DO-214AC (SMA)

Molding compound meets UL 94 V-0 flammability rating, RoHS-compliant, halogen-free

: Tin plated leads, solderable per

J-STD-002 and JESD22-B102

Cathode line denotes the cathode end

($T_a=25$ Unless otherwise specified)

Device marking code									
Maximum Repetitive Peak Reverse Voltage	VRRM	V	50	100	200	400	600	800	1000
Maximum RMS Voltage	VRMS	V	35	70	140	280	420	560	700
Maximum DC blocking Voltage	VDC	V	50	100	200	400	600	800	1000
Average rectified output current @60Hz sine wave, Resistance load, TL (FIG.1)	I _O	A	1.0						
Forward Surge Current (Non-repetitive) @60Hz Half-sine wave, 1 cycle, T _j =25	I _{FSM}	A	30						
Forward Surge Current (Non-repetitive) @1ms, square wave, 1 cycle, T _j =25			60						
Current squared time @1ms t 8.3ms T _j =25	I ² t	A ² s	3.735						
Storage temperature	T _{stg}		-55 ~ +150						
Junction temperature	T _j		-55 ~ +150						

$T_a=25$ Unless otherwise specified





$T_a=25$ Unless otherwise specified

Typical Thermal Resistance	R J-A(1)	/W	70						
	R J-A(1)		22						
	R J-L(1)		20						

Note

(1) Thermal resistance from junction to ambient and from junction to lead mounted on P.C.B. with 0.2" x 0.2" (5.0 mm x 5.0 mm) copper per



Dim	Min	Max
A	1.25	1.58
B	2.40	2.83
C	4.00	4.75
D	1.90	2.30
E	4.93	5.28
F	0.76	1.41



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